



REC'D 31 MAR 2005  
WIPO PCT



IB/05/50975



INVESTOR IN PEOPLE

The Patent Office  
Concept House  
Cardiff Road  
Newport  
South Wales  
NP10 8QQ

I, the undersigned, being an officer duly authorised in accordance with Section 74(1) and (4) of the Deregulation & Contracting Out Act 1994, to sign and issue certificates on behalf of the Comptroller-General, hereby certify that annexed hereto is a true copy of the documents as originally filed in connection with the patent application identified therein.

In accordance with the Patents (Companies Re-registration) Rules 1982, if a company named in this certificate and any accompanying documents has re-registered under the Companies Act 1980 with the same name as that with which it was registered immediately before re-registration save for the substitution as, or inclusion as, the last part of the name of the words "public limited company" or their equivalents in Welsh, references to the name of the company in this certificate and any accompanying documents shall be treated as references to the name with which it is so re-registered.

In accordance with the rules, the words "public limited company" may be replaced by p.l.c., plc, P.L.C. or PLC.

Re-registration under the Companies Act does not constitute a new legal entity but merely subjects the company to certain additional company law rules.



Signed

Dated 10 January 2005

**PRIORITY  
DOCUMENT**

SUBMITTED OR TRANSMITTED IN  
COMPLIANCE WITH RULE 17.1(a) OR (b)

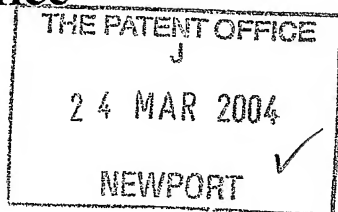


The  
Patent  
Office

24MAR04 E883383-1 002879  
P01/7700 0.00-0406540.5 NONE

1/77

**Request for grant of a patent**  
(See notes on the back of this form. You can  
also get an explanatory leaflet from the Patent  
Office to help you fill in this form)



The Patent Office

Cardiff Road  
Newport  
Gwent NP10 8QQ

1.	Your reference	PHGB 040068 GBP		
2.	Patent application number (The Patent Office will fill in this part)	0406540.5 ✓		24 MAR 2004
3.	Full name, address and postcode of the or of each applicant ( <i>underline all surnames</i> )	KONINKLIJKE PHILIPS ELECTRONICS N.V. GROENEWOUDSEWEG 1 5621 BA EINDHOVEN THE NETHERLANDS 07419294001 ✓		
	Patents ADP Number ( <i>if you know it</i> )			
	If the applicant is a corporate body, give the country/state of its incorporation	THE NETHERLANDS		
4.	Title of the invention	ELECTROLUMINESCENT DISPLAY DEVICES		
5.	Name of your agent ( <i>if you have one</i> )			
	"Address for service" in the United Kingdom to which all correspondence should be sent ( <i>including the postcode</i> )	Philips Intellectual Property & Standards Cross Oak Lane Redhill Surrey RH1 5HA		
	Patents ADP number ( <i>if you know it</i> )	08359655001 ✓		
6.	If you are declaring priority from one or more earlier patent applications, give the country and the date of filing of the or of each of these earlier applications and ( <i>if you know it</i> ) the or each application number	Country	Priority Application number	Date of filing
7.	If this application is divided or otherwise derived from an earlier UK application, give the number and the filing date of the earlier application	Number of earlier application	Date of filing (day/month/year)	
8.	Is a statement of inventorship and of right to grant of a patent required in support of this request? ( <i>Answer "Yes" if:</i> a) any applicant named in part 3 is not an inventor, or b) there is an inventor who is not named as an applicant, or c) any named applicant is a corporate body. <i>See note (d)</i> )	YES		

## Patents Form 1/77

9. Enter the number of sheets for any of the following items you are filing with this form.  
Do not count copies of the same document.

Continuation sheets of this form

Description	15
Claims(s)	4
Abstract	1
Drawings	9

10. If you are also filing any of the following, state how many against each item:

Priority Documents

Translations of priority documents

Statement of inventorship and right

to grant of a patent (*Patents Form 7/77*)

Request for preliminary examination and  
search (*Patents Form 9/77*)

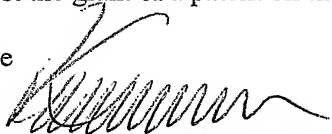
Request for substantive examination  
(*Patents Form 10/77*)

Any other documents

(*Please specify*)

11. I/We request the grant of a patent on the basis of this application.

Signature



Date

23/03/04

12. Name and daytime telephone number of  
person to contact in the United Kingdom

01293 81 5280

P L WILLIAMSON

### Warning

*After an application for a patent has been filed, the Comptroller of the Patent Office will consider whether publication or communication of the invention should be prohibited or restricted under Section 22 of the Patents Act 1977. You will be informed if it is necessary to prohibit or restrict your invention in this way. Furthermore, if you live in the United Kingdom, Section 23 of the Patents Act 1977 stops you from applying for a patent abroad without first getting written permission from the Patent Office unless an application has been filed at least 6 weeks beforehand in the United Kingdom for a patent for the same invention and either no direction prohibiting publication or communication has been given, or any such direction has been revoked.*

### Notes

- If you need help to fill in this form or you have any questions, please contact the Patent Office on 0645 500505.*
- Write your answers in capital letters using black ink or you may type them.*
- If there is not enough space for all the relevant details on any part of this form, please continue on a separate sheet of paper and write "see continuation sheet" in the relevant part(s). Any continuation sheet should be attached to this form.*
- If you have answered "Yes" Patents Form 7/77 will need to be filed.*
- Once you have filled in the form you must remember to sign and date it.*
- For details of the fee and ways to pay please contact the Patent Office.*

## DESCRIPTION

## ELECTROLUMINESCENT DISPLAY DEVICES

5 This invention relates to electroluminescent display devices, particularly active matrix display devices having an array of pixels comprising light-emitting electroluminescent display elements and thin film transistors. More particularly, but not exclusively, the invention is concerned with an active matrix electroluminescent display device whose pixels include light sensing elements  
10 which are responsive to light emitted by the display elements and used in the control of energisation of the display elements.

Matrix display devices employing electroluminescent, light-emitting, display elements are well known. The display elements commonly comprise organic thin film electroluminescent elements, (OLEDs), including polymer materials (PLEDs),  
15 or else light emitting diodes (LEDs). The term LED used below is intended to cover all of these possibilities. These materials typically comprise one or more layers of a semiconducting conjugated polymer sandwiched between a pair of electrodes, one of which is transparent and the other of which is of a material suitable for injecting holes or electrons into the polymer layer.

20 The display elements in such display devices are current driven and a conventional, analogue, drive scheme involves supplying a controllable current to the display element. Typically a current source transistor is provided as part of the pixel configuration, with the gate voltage supplied to the current source transistor determining the current through the electroluminescent (EL) display  
25 element. A storage capacitor holds the gate voltage after the addressing phase. An example of such a pixel circuit is described in EP-A-0717446.

Each pixel thus comprises the EL display element and associated driver circuitry. The driver circuitry has an address transistor which is turned on by a row address pulse on a row conductor. When the address transistor is turned on, a  
30 data voltage on a column conductor can pass to the remainder of the pixel. In particular, the address transistor supplies the column conductor voltage to the current source, comprising the drive transistor and the storage capacitor connected to the gate of the drive transistor. The column, data, voltage is

provided to the gate of the drive transistor and the gate is held at this voltage by the storage capacitor even after the row address pulse has ended. The drive transistor in this circuit is implemented as a p-channel TFT, (Thin Film Transistor) so that the storage capacitor holds the gate-source voltage fixed. This results in a fixed source-drain current through the transistor, which therefore provides the desired current source operation of the pixel. The brightness of the EL display element is approximately proportional to the current flowing through it.

In the above basic pixel circuit, differential ageing, or degradation, of the LED material, leading to a reduction in the brightness level of a pixel for a given drive current, can give rise to variations in image quality across a display. A display element that has been used extensively will be much dimmer than a display element that has been used rarely. Also, display non-uniformity problems can arise due to the variability in the characteristics of the drive transistors, particularly the threshold voltage level.

Improved voltage-addressed pixel circuits which can compensate for the ageing of the LED material and variation in transistor characteristics have been proposed. These include a light sensing element which is responsive to the light output of the display element and acts to leak stored charge on the storage capacitor in response to the light output so as to control the integrated light output of the display element during the drive period which follows the initial addressing of the pixel. Examples of this type of pixel configuration are described in detail in WO 01/20591 and EP 1 096 466. In an example embodiment, a photodiode in the pixel discharges the gate voltage stored on the storage capacitor and the EL display element ceases to emit when the gate voltage on the drive transistor reaches the threshold voltage, at which time the storage capacitor stops discharging. The rate at which charge is leaked from the photodiode is a function of the display element output, so that the photodiode serves as a light-sensitive feedback device.

The optical feedback arrangement enables compensation for initial non-uniformity between TFTs and display elements, as well as changes in these non-uniformities over time. The light output from a display element is independent of the EL display element efficiency and ageing compensation is thereby provided. Such a technique has been shown to be effective in achieving a high quality

display which suffers less from non-uniformities over a period of time. However, this method requires a high instantaneous peak brightness level to achieve adequate average brightness from a pixel in a frame time and this is not beneficial to the operation of the display as the LED material is likely to age more rapidly as a result.

In an alternative approach, the optical feedback system is used to change the duty cycle with which the display element is operated. The display element is driven to a fixed brightness, and the optical feedback is used to trigger a transistor switch which turns off the drive transistor rapidly. This avoids the need for high instantaneous brightness levels, but introduces additional complexity to the pixel.

The use of optical feedback systems is considered as an effective way of overcoming differential ageing of the LED display elements.

A path of light must be provided between the LED display element and the photo-sensitive device. One problem which arises is that any stray light which is not absorbed by the photo-sensitive device can be captured by the photosensitive device of a different pixel. The substrate can act as a light guide which increases this undesirable cross talk effect.

According to a first aspect of the invention, there is provided an active matrix display device comprising an array of display pixels, each pixel comprising:

a current-driven light emitting display element comprising an area of light emitting material sandwiched between electrodes; and

a drive transistor circuit for driving a current through the display element, the drive transistor circuit comprising a thin film circuit formed on a substrate, the thin film circuit defining a drive transistor and a light-sensitive device for detecting the brightness of the display element, the light-sensitive device having an input surface,

wherein the drive transistor is controlled in response to the light-sensitive device output and

wherein each pixel further comprises a light blocking structure formed from the thin film layers in the proximity of the light-sensitive device and substantially at the level of the input surface for preventing the passage of light to the light-sensitive device from a substantially lateral direction.

This aspect of the invention uses an additional structure at level of the input to the light-sensitive device, and this provides an effective way of blocking light from below (i.e. from the substrate) which can otherwise follow a path to the light-sensitive device, causing cross talk.

5       The light blocking structure may for example comprise a ring surrounding the light-sensitive device.

      The light blocking structure can be formed from one or more of the thin film layers defining the light-sensitive device. In this way, no additional layers are required to define the light-sensitive device, and preferably no additionally  
10       patterning steps are also required.

      The light blocking structure can be substantially planar, and can thus simply comprise an area of one or more of the thin film layers. Alternatively, the light blocking structure can comprise a side wall extending downwardly from the level of the input surface. This provides further blocking of light from very shallow angles  
15       (i.e. laterally with and with very small vertical component).

      The light blocking structure may comprise first and second light blocking elements, the first light blocking element being provided at the level of the input surface, and the second light blocking element being provided above the level of the input surface. The first light blocking element is the one described above  
20       which provides blocking of light from below, and the second light blocking element provides blocking of light directed towards the light-sensitive device laterally and from above.

      The second light blocking element can be formed from a metal layer which defines the source and drain of the drive transistor. This means that both light  
25       blocking elements can be formed from existing layers within the structure.

      The light-sensitive device is preferably formed beneath the light emitting display element.

      The invention can be used in bottom emission displays, in which the electrodes comprise a top reflective electrode and a bottom substantially  
30       transparent electrode.

      The invention can also be used in top emission displays, in which the electrodes comprise a top substantially transparent electrode and a bottom electrode which is at least partially reflective. In top emission displays, a path



needs to be provided through the bottom electrode to the underlying light-sensitive device. This may be through an aperture to allow or the bottom electrode may be semitransparent.

In another implementation, the light blocking structure can comprise an air cavity layer formed on top of the input surface of the light-sensitive device. An air cavity layer provides a light blocking function by providing reflection of all light away from the light-sensitive device other than light having a narrow range of incident angles. The light blocking structure may then further comprise an air cavity layer formed beneath a bottom surface of the light-sensitive device.

The substrate can be a glass substrate, or for top emission structures a metal foil and insulating dielectric layer.

According to a second aspect of the invention, there is provided an active matrix display device comprising an array of display pixels, each pixel comprising:

a current-driven light emitting display element comprising an area of light emitting material sandwiched between electrodes; and

a drive transistor circuit for driving a current through the display element comprising a drive transistor and a light-sensitive device for detecting the brightness of the display element, wherein the drive transistor is controlled in response to the light-sensitive device output,

wherein the electrodes comprise a top substantially transparent electrode and a bottom electrode which is at least partially reflective, and wherein a side of the substrate opposite the thin film circuitry is arranged to disturb reflection of light at the lower surface of the substrate.

This aspect of the invention provides a top emission structure, in which the bottom surface of the disturbs reflection, so that a long pipelined path of total internal reflection within the substrate is prevented.

The side of the substrate opposite the thin film circuitry can be arranged to absorb light, for example by making the surface matt black, and/or the side of the substrate opposite the thin film circuitry can be arranged to scatter light.

In all aspects, the light-dependent device preferably comprises a photodiode, for example a PIN or NIP diode stack or a Schottky diode.

Examples of the invention will now be described in detail with reference to the accompanying drawings, in which:

Figure 1 is a simplified schematic diagram of an embodiment of active matrix EL display device;

5 Figure 2 illustrates a known form of pixel circuit;

Figure 3 shows a first known optical feedback pixel design;

Figure 4 shows a second known optical feedback pixel design;

Figure 5 shows a known structure of a bottom emitting display pixel;

Figure 6 shows a known structure of a top emitting display pixel;

10 Figure 7 shows a known structure of a bottom emitting display pixel with an integrated light sensitive element;

Figure 8 is used to illustrate the problem addressed by the invention for a bottom emitting display pixel;

15 Figure 9 shows a know way to address the problem explained with reference to Figure 8;

Figure 10 shows a first bottom emitting display pixel structure of the invention;

Figure 11 shows a second bottom emitting display pixel structure of the invention;

20 Figure 12 shows a third bottom emitting display pixel structure of the invention;

Figure 13 shows a fourth bottom emitting display pixel structure of the invention;

25 Figure 14 shows a first way of providing a light path to the light-sensitive element in a top emitting display;

Figure 15 shows a second way of providing a light path to the light-sensitive element in a top emitting display;

Figure 16 shows a first top emitting display pixel structure of the invention; and

30 Figure 17 shows a second top emitting display pixel structure of the invention.

The same reference numbers are used throughout the Figures to denote the same or similar parts.

Figure 1 shows a known active matrix electroluminescent display device. The display device comprises a panel having a row and column matrix array of regularly-spaced pixels, denoted by the blocks 1 and comprising electroluminescent display elements 2 together with associated switching means, located at the intersections between crossing sets of row (selection) and column (data) address conductors 4 and 6. Only a few pixels are shown in the Figure for simplicity. In practice there may be several hundred rows and columns of pixels. The pixels 1 are addressed via the sets of row and column address conductors by a peripheral drive circuit comprising a row, scanning, driver circuit 8 and a column, data, driver circuit 9 connected to the ends of the respective sets of conductors.

The electroluminescent display element 2 comprises an organic light emitting diode, represented here as a diode element (LED) and comprising a pair of electrodes between which one or more active layers of organic electroluminescent material is sandwiched. The display elements of the array are carried together with the associated active matrix circuitry on one side of an insulating support. Either the cathodes or the anodes of the display elements are formed of transparent conductive material. The support is of transparent material such as glass and the electrodes of the display elements 2 closest to the substrate may consist of a transparent conductive material such as ITO so that light generated by the electroluminescent layer is transmitted through these electrodes and the support so as to be visible to a viewer at the other side of the support.

Figure 2 shows in simplified schematic form the most basic pixel and drive circuitry arrangement for providing voltage-addressed operation. Each pixel 1 comprises the EL display element 2 and associated driver circuitry. The driver circuitry has an address transistor 16 which is turned on by a row address pulse on the row conductor 4. When the address transistor 16 is turned on, a voltage on the column conductor 6 can pass to the remainder of the pixel. In particular, the address transistor 16 supplies the column conductor voltage to a current source 20, which comprises a drive transistor 22 and a storage capacitor 24. The column voltage is provided to the gate of the drive transistor 22, and the gate is held at this voltage by the storage capacitor 24 even after the row address pulse has ended.

The drive transistor 22 in this circuit is implemented as a p-type TFT, so that the storage capacitor 24 holds the gate-source voltage fixed. This results in a fixed source-drain current through the transistor, which therefore provides the desired current source operation of the pixel.

5 In the above basic pixel circuit, for circuits based on polysilicon, there are variations in the threshold voltage of the transistors due to the statistical distribution of the polysilicon grains in the channel of the transistors. Polysilicon transistors are, however, fairly stable under current and voltage stress, so that the threshold voltages remain substantially constant.

10 The variation in threshold voltage is small in amorphous silicon transistors, at least over short ranges over the substrate, but the threshold voltage is very sensitive to voltage stress. Application of the high voltages above threshold needed for the drive transistor causes large changes in threshold voltage, which changes are dependent on the information content of the displayed image. There  
15 will therefore be a large difference in the threshold voltage of an amorphous silicon transistor that is always on compared with one that is not. This differential ageing is a serious problem in LED displays driven with amorphous silicon transistors.

In addition to variations in transistor characteristics there is also differential ageing in the LED itself. This is due to a reduction in the efficiency of the light  
20 emitting material after current stressing. In most cases, the more current and charge passed through an LED, the lower the efficiency.

Figures 3 and 4 show examples of pixel layout with optical feedback to provide ageing compensation.

25 In the pixel circuit of Figure 3, a photodiode 27 discharges the gate voltage stored on the capacitor 24 ( $C_{data}$ ), causing the brightness to reduce. The display element 2 will no longer emit when the gate voltage on the drive transistor 22 ( $T_{drive}$ ) reaches the threshold voltage, and the storage capacitor 24 will then stop discharging. The rate at which charge is leaked from the photodiode 27 is a function of the display element output, so that the photodiode 27 functions as a  
30 light-sensitive feedback device. Once the drive transistor 22 has switched off, the display element anode voltage reduces causing the discharge transistor 29 ( $T_{discharge}$ ) to turn on, so that the remaining charge on the storage capacitor 24 is rapidly lost and the luminance is switched off.

As the capacitor holding the gate-source voltage is discharged, the drive current for the display element drops gradually. Thus, the brightness tails off. This gives rise to a lower average light intensity.

Figure 4 shows a circuit which has been proposed by the applicant, and  
5 which has a constant light output and then switches off at a time dependent on the light output.

The gate-source voltage for the drive transistor 22 is again held on a storage capacitor 24 ( $C_{\text{store}}$ ). However, in this circuit, this capacitor 24 is charged to a fixed voltage from a charging line 32, by means of a charging transistor 34.  
10 Thus, the drive transistor 22 is driven to a constant level which is independent of the data input to the pixel when the display element is to be illuminated. The brightness is controlled by varying the duty cycle, in particular by varying the time when the drive transistor is turned off.

The drive transistor 22 is turned off by means of a discharge transistor 36  
15 which discharges the storage capacitor 24. When the discharge transistor 36 is turned on, the capacitor 24 is rapidly discharged and the drive transistor turned off.

The discharge transistor 36 is turned on when the gate voltage reaches a sufficient voltage. A photodiode 27 is illuminated by the display element 2 and again generates a photocurrent in dependence on the light output of the display  
20 element 2. This photocurrent charges a discharge capacitor 40 ( $C_{\text{data}}$ ), and at a certain point in time, the voltage across the capacitor 40 will reach the threshold voltage of the discharge transistor 36 and thereby switch it on. This time will depend on the charge originally stored on the capacitor 40 and on the photocurrent, which in turn depends on the light output of the display element.  
25 The discharge capacitor initially stores a data voltage, so that both the initial data and the optical feedback influence the duty cycle of the circuit.

There are many alternative implementations of pixel circuit with optical feedback. Figures 3 and 4 show p-type implementations, and there are also n-type implementations, for example for amorphous silicon transistors.

30 Figure 5 shows the known basic bottom emission structure including the active matrix.

The device comprises a substrate 60 over which the drive transistor semiconductor body 62 is deposited. A gate oxide dielectric layer 64 covers the

semiconductor body, and a top gate electrode 66 is provided over the gate dielectric layer 64.

5 A first insulating layer 68 (typically silicon dioxide or silicon nitride) provides spacing between the gate electrode (which typically also forms row conductors) and the source and drain electrodes. These source and drain electrodes are defined by a metal layer 70 over the insulator layer 68, and the electrodes connect to the semiconductor body through vias as shown.

10 A second insulating layer 72 (again typically silicon dioxide or silicon nitride) provides spacing between the source and drain electrodes (which typically also form column conductors) and the LED anode. The LED anode 74 is provided over the second insulating layer 72.

In the case of a bottom emission display as shown in Figure 5, this bottom anode needs to be at least partially transparent, and ITO is typically used.

15 The EL material 76 is formed in a well over the anode, and is preferably deposited by printing. Separate sub-pixels are formed for the three primary colours, and a print dam 78 assists in the accurate printing of the different EL materials.

20 The print dam 78 enables printing of separate pixels. This dam layer is generally made of an insulating polymer and has a height of several microns. A common cathode 80 is provided over the display, and this is reflective and at a common potential for all pixels (ground in Figure 2).

25 Figure 6 shows the basic known top emission structure including the active matrix. The structure is essentially the same as in Figure 5, but the anode 74a is reflective and the cathode 80a is transmissive. The cathode may again be formed from ITO, but may have a thin metal or silicide coating between the ITO and polymer to control the barrier for electron injection. For example, this may be a thin layer of Barium. Protection and encapsulation layers 82 cover the display.

30 In a top-emission display, a transparent cathode is needed. The cathode does, however, have to be highly conductive, and at present highly conductive transparent metals are not readily available. Therefore the cathode of top-emission displays comprises a (semi-) transparent layer on top of the emissive pixel part and shunted with a lower resistance conducting (non-transparent) metal

79. By placing this highly conductive metal 79 on top of the dam 78 as shown, there is no loss in pixel aperture.

The anode metal must be a high work function metal, and it is known to provide a layer of ITO on top of a reflective metal to achieve a high work function  
5 into the LED stack.

Figure 7 shows the integration of an amorphous silicon PIN/NIP photo-diode 84 in a bottom emitting structure, in conventional manner. This type of photo-sensor is preferred as the amorphous silicon has high quantum efficiency for photo absorption.

10 This type of photo-sensor is ideal for bottom emission as the gate metal which is used to form the bottom electrode 86 of the diode stack screens the photo-sensor from external light 87. An open top aperture of the diode stack, shown schematically in Figure 7, allows in light from the LED as shown by arrow 88.

15 The photodiode requires screening from both ambient light and stray LED light from neighbouring pixels. The glass substrate together with its dielectric coatings provides a very good light pipe for light at shallow angles up to the critical angle for internal reflection. Consequently, significant amounts of light can reach photodiodes in pixels large distances away from the source of light.

20 Figures 8 to 17 show the display structure more schematically, and the top layers, which are not relevant to the invention, are removed, as well as the drive transistor structure. Thus, Figures 8 to 17 are intended to represent more schematically the structures shown in more detail in Figures 5 to 7.

25 Figure 8 is used to illustrate the problem associated with the known downward emitting LED structure of Figure 7, and again shows the small photodetector 84 under part of the ITO pixel aperture.

30 As shown in Figure 8, the substrate glass and dielectrics have a refractive index close to 1.5, and so light entering at angles less than a critical angle (around 30-40° to the normal) becomes trapped, reflecting back and forwards between the metal cathode 80, and the air interface on bottom side of the substrate. This is shown by rays such as (c) in Figure 8. Light at steeper angles (namely closer to normal to the substrate), rays (a), leave the display as required to give an image.

Light of all angles, such as rays (b), can couple into the photodiode and ambient light of all angles can also couple into the photodiode, such as rays (d).

Figure 9 shows a known solution to shield from as much stray light as possible by using a bottom light shield 90. The photodiode 84 is spaced vertically from this shield by a dielectric of thickness at least 0.5micron, and consequently very shallow angle light such as shown by rays (f), (g) and (h) can still enter the device. Clearly, the overlap of the shield can be extended to cut down the angle of light getting to the diode. However, extending this overlap consumes aperture, and it is not practical to shield light at angles of around 0-10° to the substrate.

The invention provides a light blocking structure formed from the existing thin film layers and in the proximity of the light-sensitive device, in particular at the level of the input surface, for preventing the passage of light to the light-sensitive device from a substantially lateral direction. The light blocking structure can be formed around the photodiodes at various levels in the device stack to absorb or reflect this very shallow angle light. By "shallow" is meant close the plane of the substrate, i.e. laterally directed light.

Figure 10 shows a first arrangement of light blocking arrangement of the invention. A ring of absorbing material 100 is placed around the diode 84 at the same vertical level to protect from rays (g). The material of the ring 100 can be the photodiode material, so that no additional processing is required. The light blocking arrangement 100 is used in combination with the (conventional) light shield 90. In the example of Figure 10, the light blocking structure is substantially planar, and can thus simply comprise an area of one or more of the thin film layers of the photodiode.

Figure 11 shows how extra processing can also prevent rays (f). In this case, vias are formed to the light shield 90, so that the light blocking structure has side walls 110 extending downwardly from the top of the photodiode stack. These side walls extend the full height of the photodiode stack.

Figure 12 shows how other thin film layers can similarly be patterned at no extra processing cost to prevent rays from laterally above reaching the photodiode. Figure 12 shows in particular an additional light blocking element formed from the metal layer 70 which defines the source and drain of the drive transistor. The light blocking structure then has first 100 and second 120 light



blocking elements, the second light blocking element being provided above the level of the input surface of the photodiode. The second light blocking element 120 provides blocking of light directed towards the light-sensitive device laterally and from above as shown by rays (h).

5       The light blocking structure can still be formed from existing layers within the structure.

Figure 13 shows how the light blocking structure can be formed as air cavities 130, and these can provide protection from all shallow angle, with only large angle LED and ambient light (d) being detected. This could be combined  
10 with top or bottom light shields to produce a sensor which detects only pixel light, or only ambient light. These cavities 130 can be formed by under-etching metal or dielectric films placed in the stack. The cavities do not fully under-etch in other directions, so that the photodiodes remain anchored to the other layers and connecting electrodes. The diode semiconductor can be provided with a thin  
15 surface dielectric between it and the cavity, so that the surface is properly passivated. Cavities may effectively be produced without air gaps by using more complex dielectric stacks with high and low refractive indices.

The positioning of the sensor beneath the anode layer is clearly not appropriate for top emission structures as shown in Figure 6, in which the anode is  
20 a reflective and opaque metal.

Figure 14 shows a first way to provide a path of light to the photodiode for an upward emitting structure, in which a semitransparent cathode 140 is used. However, shallow angle light (light rays (b) from the LED and ambient light rays (c)) can again be piped in the glass and carried long distances.

25       This can be reduced by having just a small transparent aperture over the diode as shown in Figure 15. A top shield 152 is also shown in Figure 15 for shielding ambient light, and if this is a metal, can also be used to back-up the cathode electrically as shown at 154, because the cathode is made from a relatively low conductivity transparent material, such as ITO. Light rays (d) can  
30 still enter the substrate and be piped within it.

Figure 16 shows how the invention enables the diode material to be used to from a light blocking structure 160 again to absorb the remaining shallow angle light rays (d) in a similar fashion to the downward emission structures described

above. Clearly, the other refinements and modifications above are equally applicable to top emitting structures.

In upward emitting displays, there is no need for transparent substrates. The substrates can be made on transparent substrates such as glass or plastic, but they may also be made on opaque substrates such as metal foil.

Figure 17 shows a further approach which may be used alone or in combination with the techniques described above to prevent light being piped long distances in the glass substrate for top emission structures where the substrate does not need to be transparent.

The underlying surface 170 of the glass or insulator is made to be matt black (to absorb light) and/or rough in order to scatter the light into large angles which can pass back out.

The use of a scattering surface is particularly useful with metal foils where a surface texture can easily be applied during substrate polishing. The horizontal and vertical pitch of the roughness should be 0.2-2.0  $\mu\text{m}$  and the optimum angle for scattering is 35-45°.

The light scattered back to steep angles can then be absorbed in a thin film layer (e.g. the photodiode material). Typically, the planarising dielectric (polymer or spin on glass) on the steel is 2-10  $\mu\text{m}$  thick, and the pixel to pixel spacing is 50-500  $\mu\text{m}$ .

A further improvement can be made in the examples above by preventing light entering the photodiode from the side through the design of the photodiode. This can be achieved by coating the photodiode sidewall with a black pigment, and one way to do this is by anisotropic etching.

The layers used in the manufacture of the thin film transistors of each pixel (for example amorphous silicon or low temperature polysilicon layers) are also absorbing, and these layers may also be structured to perform light blocking functions.

The shielding provided by the invention reduces cross talk between light sensors of different pixels. This can also be an advantage for image sensor applications. For example, the light-sensitive elements of the optical feedback pixels can also be controlled to provide an image sensing function in a multifunction display. The light shielding then also improves the image sensing

performance. The approach of the invention may also be applied to sold state image sensor devices which have no display functionality.

From reading the present disclosure, other modifications will be apparent to persons skilled in the art. Such modifications may involve other features which are  
5 already known in the field of active matrix EL display devices and component parts therefor and which may be used instead of or in addition to features already described herein. Although claims have been formulated in this application to particular combinations of features, it should be understood that the scope of the disclosure of the present application also includes any novel feature or any novel  
10 combination of features disclosed herein either explicitly or implicitly or any generalisation thereof, whether or not it relates to the same invention as presently claimed in any claim and whether or not it mitigates any or all of the same technical problems as does the present invention. The applicants hereby give notice that new claims may be formulated to such features and/or combinations of  
15 such features during the prosecution of the present application or of any further application derived therefrom.



## CLAIMS

1. An active matrix display device comprising an array of display pixels, each pixel comprising:

5 a current-driven light emitting display element comprising an area of light emitting material sandwiched between electrodes; and

a drive transistor circuit for driving a current through the display element, the drive transistor circuit comprising a thin film circuit formed on a substrate, the thin film circuit defining a drive transistor and a light-sensitive device for detecting  
10 the brightness of the display element, the light-sensitive device having an input surface,

wherein the drive transistor is controlled in response to the light-sensitive device output and

wherein each pixel further comprises a light blocking structure formed from  
15 the thin film layers in the proximity of the light-sensitive device and substantially at the level of the input surface for preventing the passage of light to the light-sensitive device from a substantially lateral direction.

2. A device as claimed in claim 1, wherein the light blocking structure  
20 comprises a ring surrounding the light-sensitive device.

3. A device as claimed in claim 1 or 2, wherein the light blocking structure is formed from one or more of the thin film layers defining the light-sensitive device.

25 4. A device as claimed in any preceding claim, wherein the light blocking structure is substantially planar.

5. A device as claimed in any one of claims 1 to 3, wherein the light blocking structure comprises a side wall extending downwardly from the level of the input  
30 surface.

6. A device as claimed in any preceding claim, wherein the light blocking structure comprises first and second light blocking elements, the first light blocking

element being provided at the level of the input surface, and the second light blocking element being provided above the level of the input surface.

7. A device as claimed in claim 6, wherein the first light blocking element is formed from one or more of the thin film layers defining the light-sensitive device, and the second light blocking element is formed from a metal layer which defines the source and drain of the drive transistor.

8. A device as claimed in any preceding claim, wherein the light-sensitive device is formed beneath the light emitting display element.

9. A device as claimed in claim 8, wherein the electrodes comprise a top reflective electrode and a bottom substantially transparent electrode.

10. A device as claimed in claim 8, wherein the electrodes comprise a top substantially transparent electrode and a bottom electrode which is at least partially reflective.

11. A device as claimed in claim 10, wherein the bottom electrode is substantially fully reflective and comprises an aperture to allow the passage of light through the bottom electrode to the light-sensitive device.

12. A device as claimed in claim 10, wherein the bottom electrode is semitransparent to allow the passage of light through the bottom electrode to the light-sensitive device.

13. A device as claimed in claim 1, wherein the light blocking structure comprises a refractive index cavity formed on top of the input surface of the light-sensitive device.

14. A device as claimed in claim 13, wherein the refractive index cavity comprises an air cavity.

15. A device as claimed in claim 14, wherein the light blocking structure further comprises an air cavity layer formed beneath a bottom surface of the light-sensitive device.

5 16. A device as claimed in any preceding claim, wherein the substrate comprises a glass substrate.

10 17. A device as claimed in any one of claims 10 to 12, wherein the substrate comprises a metal foil and insulating dielectric layer.

18. A device as claimed in any one of claims 10 to 12 or 17, wherein a side of the substrate opposite the thin film circuitry is arranged to disturb reflection of light at the lower surface of the substrate.

15 19. A device as claimed in claim 18, wherein the side of the substrate opposite the thin film circuitry is arranged to absorb light.

20 20. A device as claimed in claim 18 or 19 wherein the side of the substrate opposite the thin film circuitry is arranged to scatter light.

21. An active matrix display device comprising an array of display pixels, each pixel comprising:

25 a current-driven light emitting display element comprising an area of light emitting material sandwiched between electrodes; and

a drive transistor circuit for driving a current through the display element comprising a drive transistor and a light-sensitive device for detecting the brightness of the display element, wherein the drive transistor is controlled in response to the light-sensitive device output,

30 wherein the electrodes comprise a top substantially transparent electrode and a bottom electrode which is at least partially reflective, and wherein a side of the substrate opposite the thin film circuitry is arranged to disturb reflection of light at the lower surface of the substrate.

22. A device as claimed in claim 21, wherein the side of the substrate opposite the thin film circuitry is arranged to absorb light.

5 23. A device as claimed in claim 22, wherein the side of the substrate opposite the thin film circuitry is matt black.

24. A device as claimed in claim 21, 22 or 23, wherein the side of the substrate opposite the thin film circuitry is arranged to scatter light.

10

25. A device as claimed in any preceding claim, wherein the light-dependent device comprises a photodiode.

15

26. A device as claimed in claim 25, wherein the photodiode comprises a PIN or NIP diode stack or a Schottky diode and top and bottom contact terminals.

27. A device as claimed in any preceding claim, further comprising a light shield layer at the base of the light-sensitive device.

20

28. A device as claimed in claim 27, wherein the light shield layer has a footprint larger than the footprint of the light sensitive device.



## ABSTRACT

## ELECTROLUMINESCENT DISPLAY DEVICES

An active matrix display device has pixels each with a light-sensitive device (84)  
5 for optical feedback functions. Each pixel has a light blocking structure (100)  
formed from the thin film layers of the display substrate in the proximity of the light-  
sensitive device (84) and substantially at the level of an input surface of the light  
sensitive device. This structure prevents the passage of light (g) to the light-  
sensitive device from a substantially lateral direction.

10 [Fig. 10]



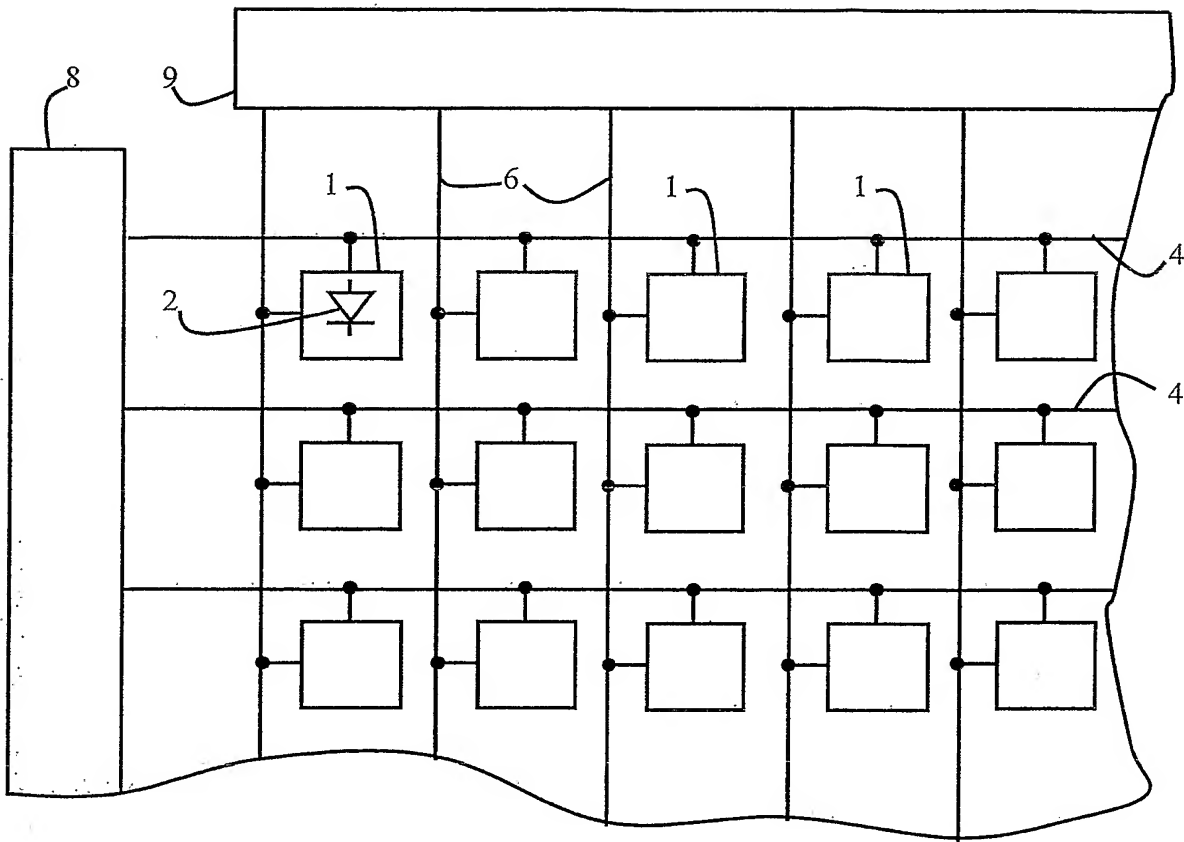
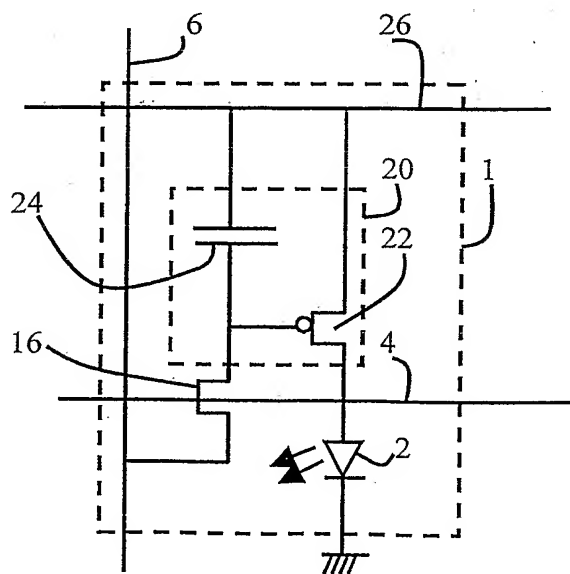


FIG. 1 PRIOR ART



PRIOR ART FIG. 2



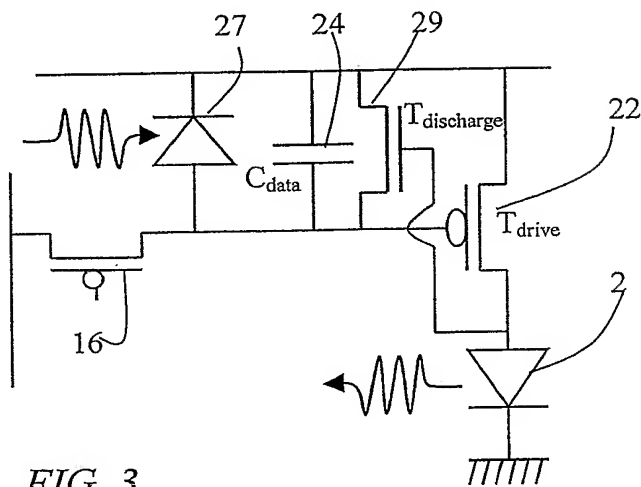


FIG. 3  
PRIOR ART

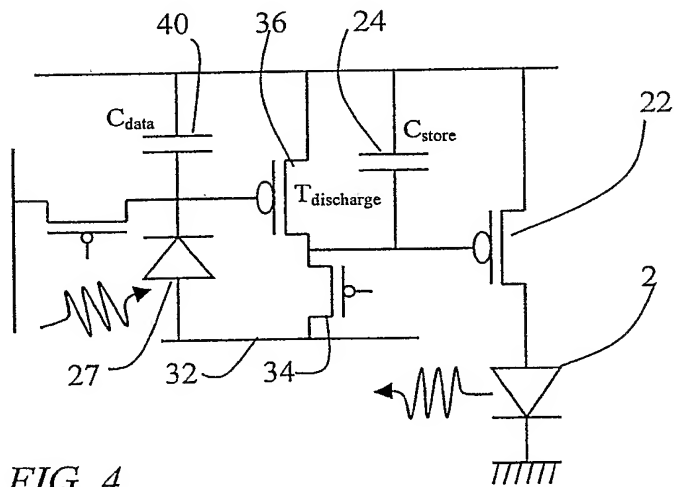


FIG. 4  
PRIOR ART



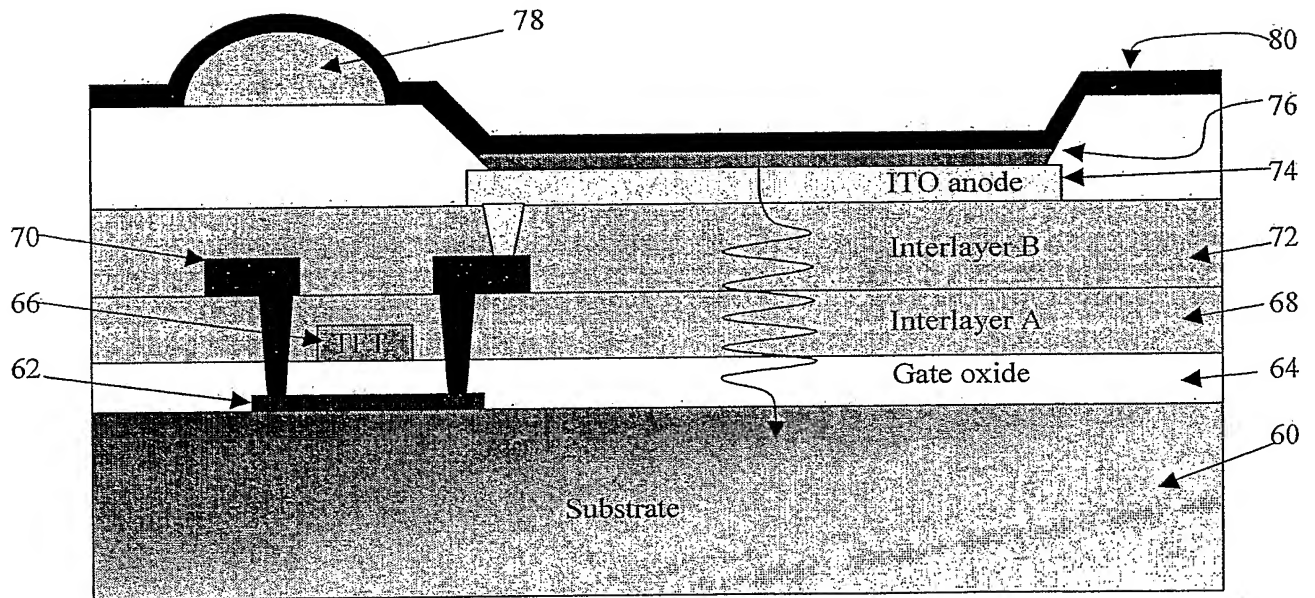


FIG. 5 PRIOR ART

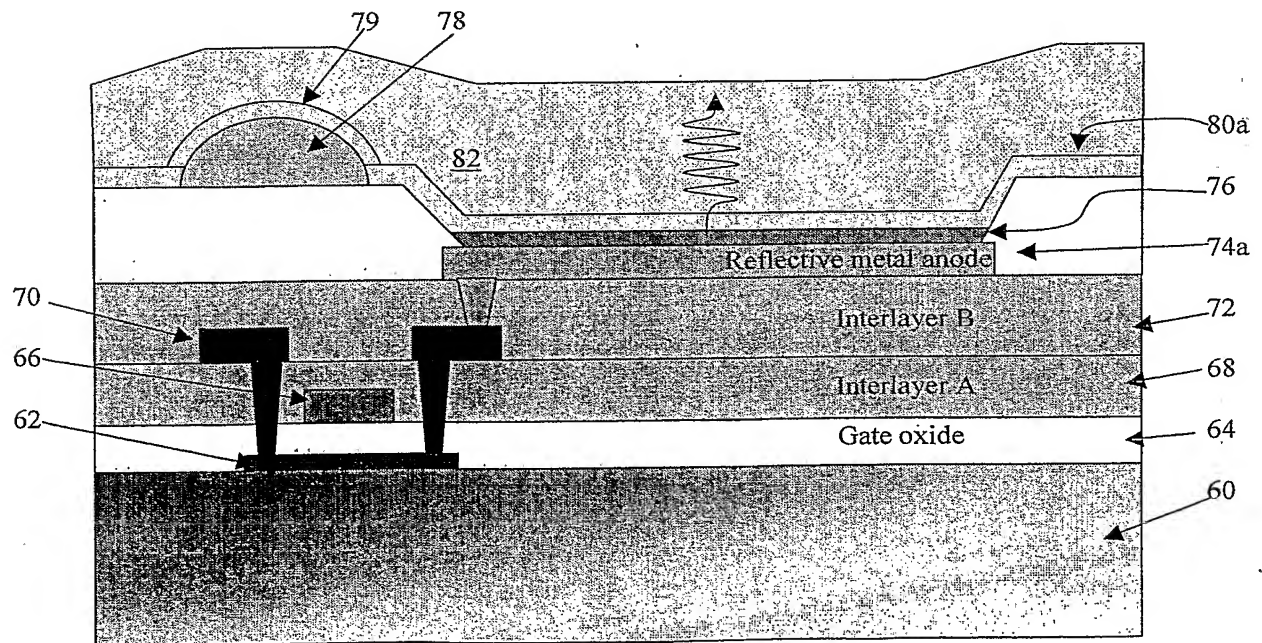


FIG. 6 PRIOR ART



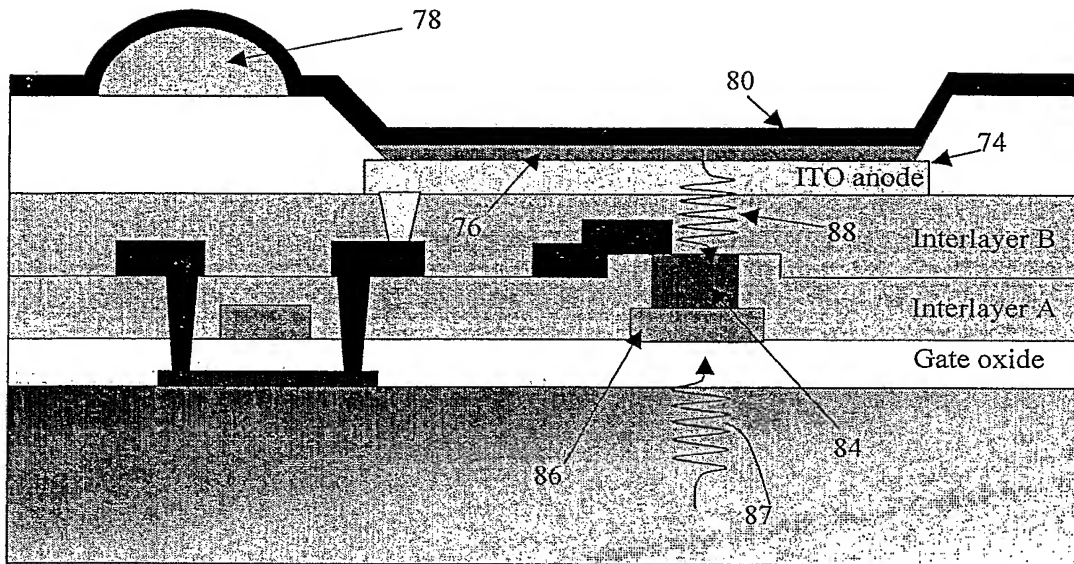


FIG. 7 PRIOR ART

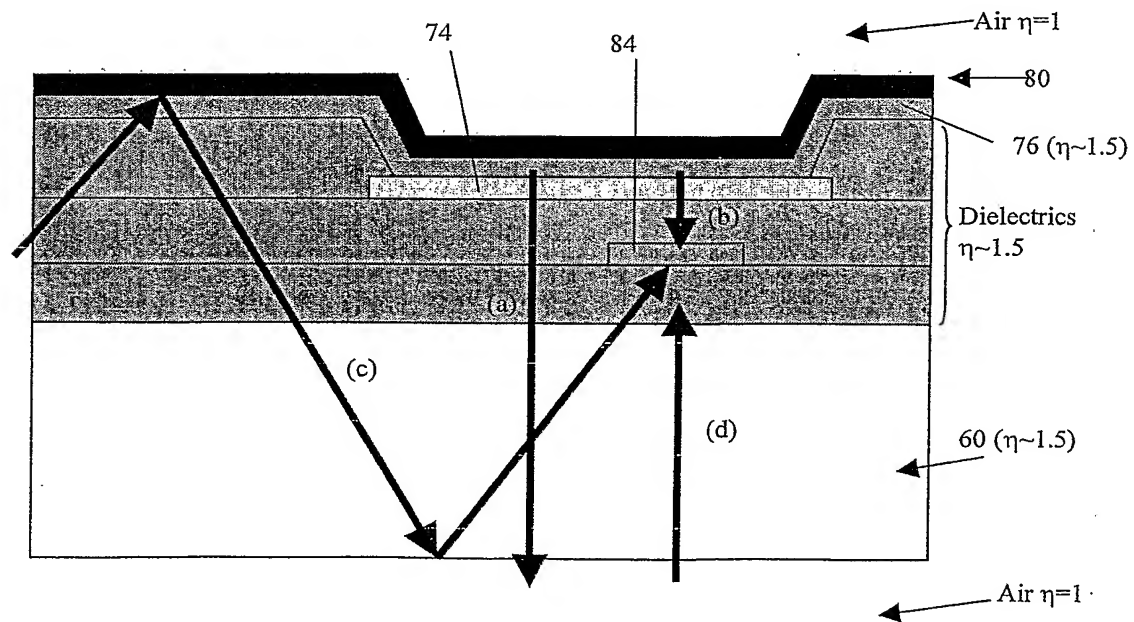
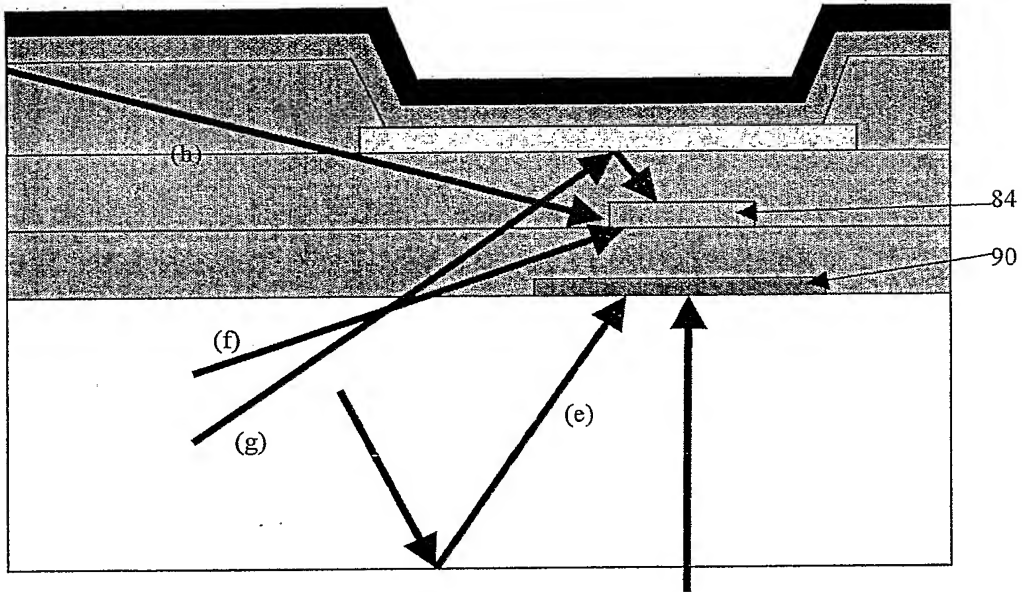


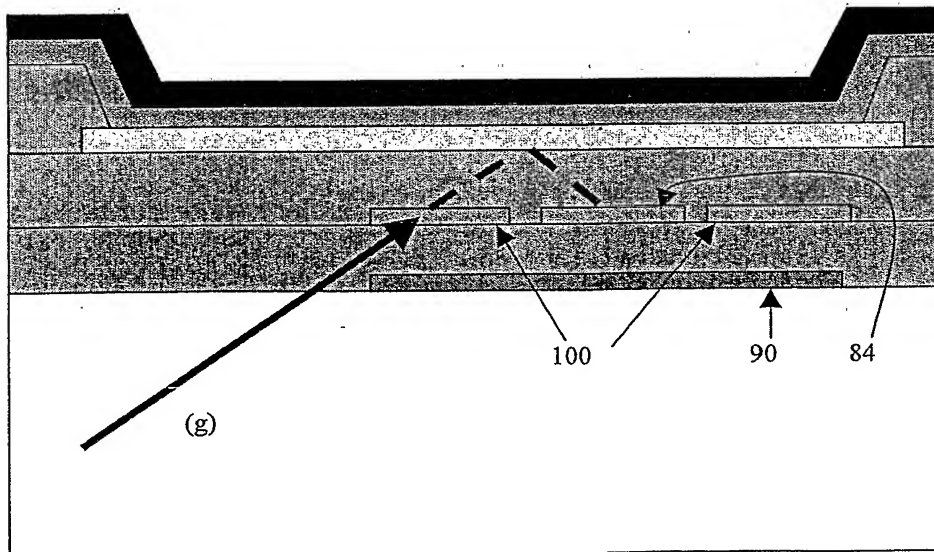
FIG. 8  
PRIOR ART





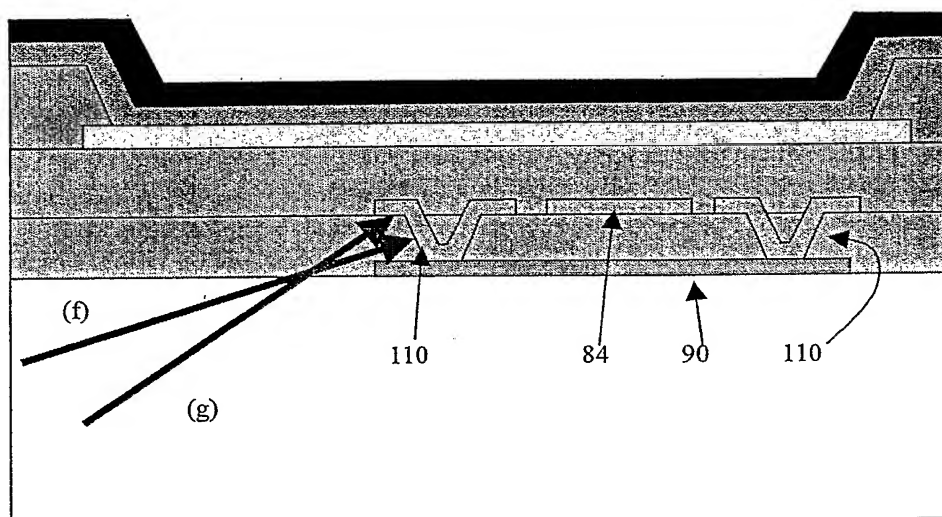
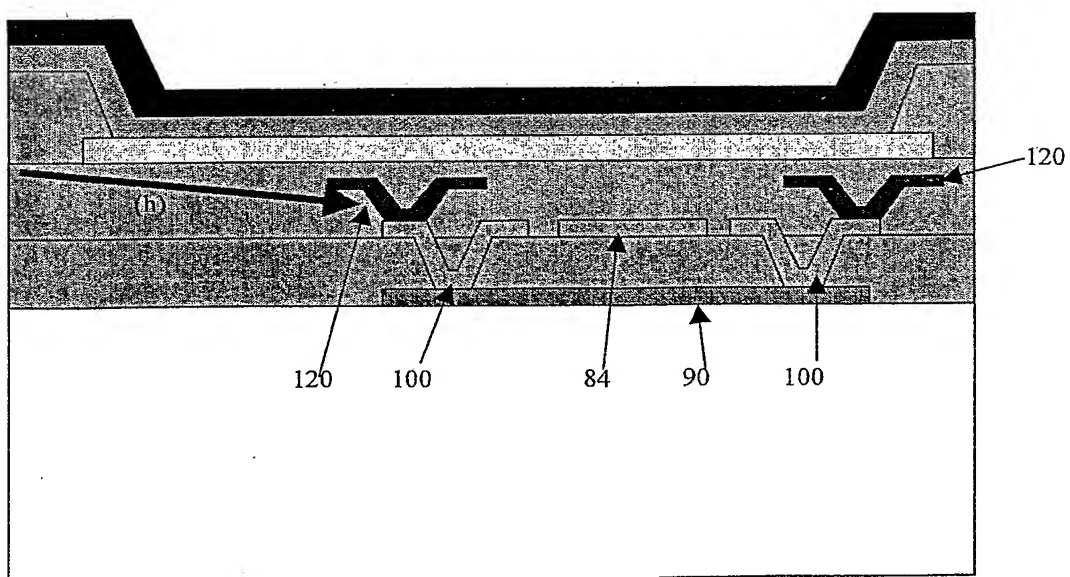


*FIG. 9*  
*PRIOR ART*

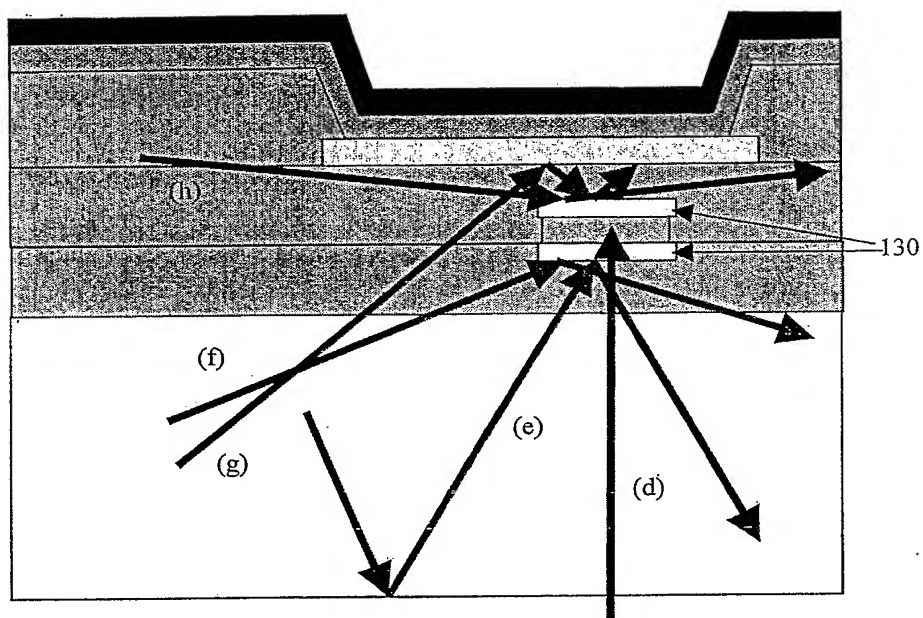
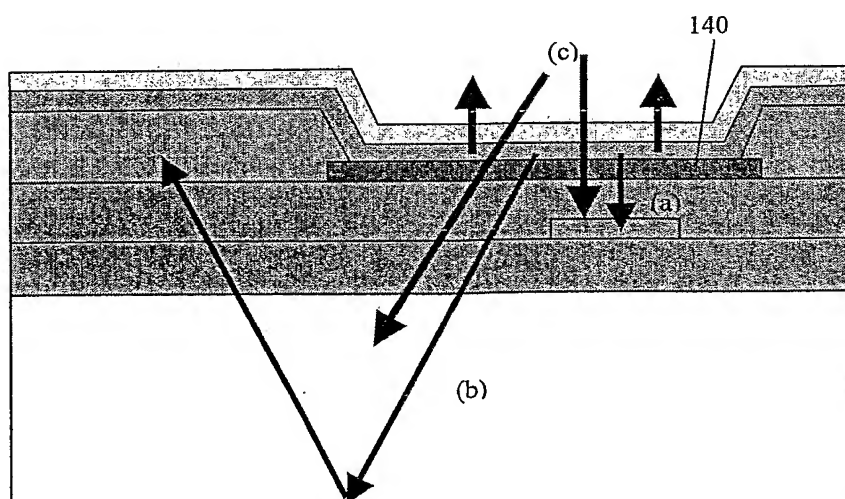


*FIG. 10*



*FIG. 11**FIG. 12*



*FIG. 13**FIG. 14*



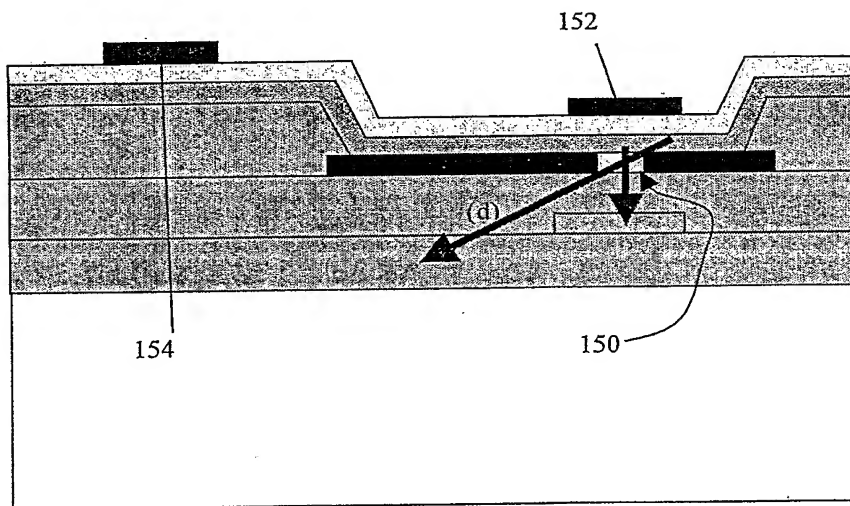


FIG. 15

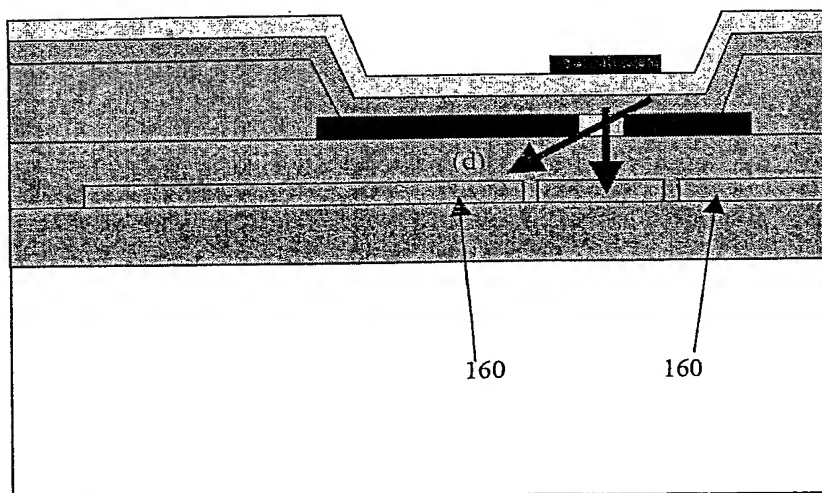
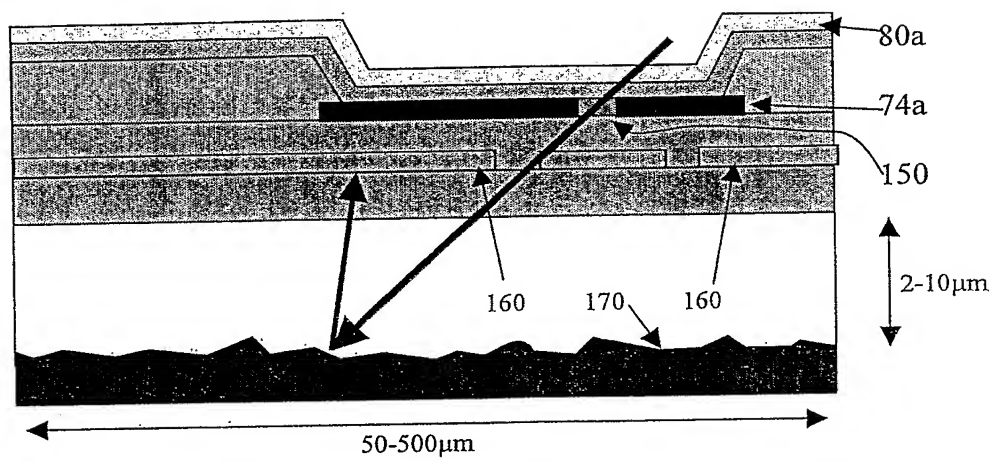


FIG. 16





*FIG. 17*

PCT/IB2005/050975

